

## uM-FPU64/uM-FPU V3.1 Comparison Chart

Features	uM-FPU64	uM-FPU V3.1
<b>Packages</b>	DIP-28 SOIC-28 TQFP-44	DIP-18 SOIC-18
<b>Supply Voltage</b>	3.3V	5V 3.3V 2.7V
<b>Operating Speed</b>	40 MIPS @ 3.3V	30 MIPS @ 5V 15 MIPS @ 3.3V 7.5 MIPS @ 2.7V
<b>SPI Interface</b>	√	√
<b>I<sup>2</sup>C Interface</b>	√	√
<b>5V Tolerant Pins</b>	√	
<b>Floating Point Math</b>	32-bit 64-bit	32-bit
<b>Integer Math</b>	32-bit 64-bit	32-bit
<b>32-bit Registers</b>	128	128
<b>64-bit Registers</b>	128	-
<b>Flash Memory</b>	2048 bytes	2304 bytes
<b>EEPROM</b>		1024 bytes
<b>RAM</b>	2304 bytes	-
<b>Analog Inputs</b>	6 (28-pin) 9 (44-pin)	2
<b>Analog Voltage Reference</b>	VREF+, VREF-	-
<b>Digital Input/Output</b>	9 (28-pin) 23 (44-pin)	2 (output only)
<b>Real-Time Clock</b>	√	-
<b>Local Device Support</b>	RAM / FIFO 1-Wire local bus I <sup>2</sup> C local bus SPI local bus UART (with flow control) Counter (with debounce) Servo controllers LCD (4-bit parallel) VDrive2	-
<b>Background Processing</b>	√	-
<b>Event Handling</b>	√	-
<b>Extended Instruction Set</b>	√	-
<b>Firmware Upgrades</b>	√	-
<b>Price</b>	\$24.95	\$19.95